



2836

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PTO/SB/82 (10-00)

Approved for use through 10/31/2002. OMB 0651-0035

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REVOCATION OF POWER OF ATTORNEY OR AUTHORIZATION OF AGENT

Application Number	10/082,050
Filing Date	2/14/2002
First Named Inventor	Stephen R. Cebenko
Group Art Unit	2836
Examiner Name	
Attorney Docket Number	1-1-3-1-1

I hereby revoke all previous powers of attorney or authorizations of agent given in the above-identified application:

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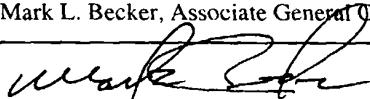
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I am the:

Applicant/Inventor.

Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)

SIGNATURE of Applicant or Assignee of Record

Name	Mark L. Becker, Associate General Counsel, IP
Signature	
Date	5/11/03

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

*Total of _____ forms are submitted.

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PTO/SB/81 (02-01)

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POWER OF ATTORNEY OR AUTHORIZATION OF AGENT

Application Number	10/082,050
Filing Date	2/14/2002
First Named Inventor	Stephen R. Cebenko
Title	Integrated Circuit Base...
Group Art Unit	2836
Examiner Name	To Be Assigned
Attorney Docket Number	1-1-3-1-1

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Name	Registration Number

as my/our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

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I am the:

Applicant/Inventor.

Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

SIGNATURE of Applicant or Assignee of Record

Name Mark L. Becker, Associate General Counsel, IP

Signature Mark L. Becker

Date 5/11/03

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

*Total of _____ forms are submitted.

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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Lattice Semiconductor Corporation
Application No./Patent No.: 10/082,050 Filed/Issue Date: 2/14/2002
Entitled: Integrated Circuit Base Transistor Structure And Associated Programmable Cell Library
Lattice Semiconductor Corporation, a Delaware Corporation
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

- the assignee of the entire right, title, and interest; or
- an assignee of less than the entire right, title and interest.
The extent (by, percentage) of its ownership interest is _____ %

in the patent application/patent identified above by virtue of either:

A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 012623, Frame 0840, or for which a copy thereof is attached.

OR

B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: _____ To: _____
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Additional documents in the chain of title are listed on a supplemental sheet.

Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

5/11/03
Date

Mark L. Becker

Typed or printed name

Mark L. Becker

Signature

Associate General Counsel, IP

Title